

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2811

Examiner: Not yet assigned

Re PATENT APPLICATION of

Applicant(s): Kazutaka SHIBATA

Serial No.: 10/676,050

Filed: October 2, 2003

For: SEMICONDUCTOR DEVICE

Docket No.: AI 269 D1

INFORMATION
DISCLOSURE
STATEMENT

March 11, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure, attached is Form PTO-1449 which lists references cited in the parent application **09/970,193**. Any relevance of these references can be gleaned from the parent application. The Examiner may wish to consult this parent application during examination of this Divisional Application.

Since this Information Disclosure Statement is being filed before the First Office Action on the merits, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to all listed documents. Consideration of the listed documents is respectfully requested.


Respectfully submitted,

Robert H. Berdo, Jr.
Registration No. 38,075
RABIN & BERDO, P.C.
CUSTOMER NO. 23995
Telephone: 202-371-8976
Facsimile: 202-408-0924

March 15, 2004
Date

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RHB:crh

FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT 	Atty. Docket: AI 269 D1	Serial No. 10/676,050
	Applicant: Kazutaka SHIBATA	
	Filing Date: October 2, 2003	Group Art Unit: 2811

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date
	AA	US 6399423B2	06-2002	Matsuura et al.			
	AB	US 6437429B1	08-2002	Su et al.			
	AC	US 6201294B1	03-2001	Lee			
	AD						
	AE						
	AF						
	AG						
	AH						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub-Class	Translation
	AI						
	AJ						
	AK						
	AL						
	AM						

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	
	AO	

Examiner

Date Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.